



#9  
4/29/03  
AM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 9664

NAKAGAWA, et al.

Art Unit: 2818

Serial No.: 09/810,499

Examiner: C. Yoha

Filed: March 19, 2001

Docket No.: 100353-00049

For: SEMICONDUCTOR MEMORY DEVICE WITH SHORTER SIGNAL  
LINES

**REQUEST FOR APPROVAL OF DRAWING CORRECTIONS**

Commissioner for Patents  
Washington, D.C. 20231

April 21, 2003

Dear Sir:

The Examiner's approval of the drawings corrections to Fig. 1 shown in red ink  
on the attached copy, is respectfully requested.

Should any fees be due with respect to this paper, please charge counsel's  
Deposit Account No. 01-2300.

Respectfully submitted,

Arent Fox Kintner Plotkin & Kahn, PLLC

Raymond J. Ho  
Attorney for Applicants  
Registration No. 41,838

Customer No. 004372  
1050 Connecticut Avenue, N.W., Suite 400  
Washington, D.C. 20036-5339  
Tel: (202) 857-6000  
Fax: (202) 638-4810  
RJH/kf  
Enclosures: Marked-Up Figure 1